

ABSTRACT

It is an object of the present invention to provide a memory controller which prevents successive access to the same
5 bank of SDRAM and improves processing time. A memory controller (105) of the present invention is a memory controller for controlling memory which has a plurality of banks and can make successive access in bank split mode. The
10 memory controller (105) controls the priority of a plurality of blocks in such a way that memory access requests from blocks (804, 805, and 806), which make access to SDRAM (808) via the memory controller (105), are caused to access the different banks of the SDRAM (808).